

FIG. 1

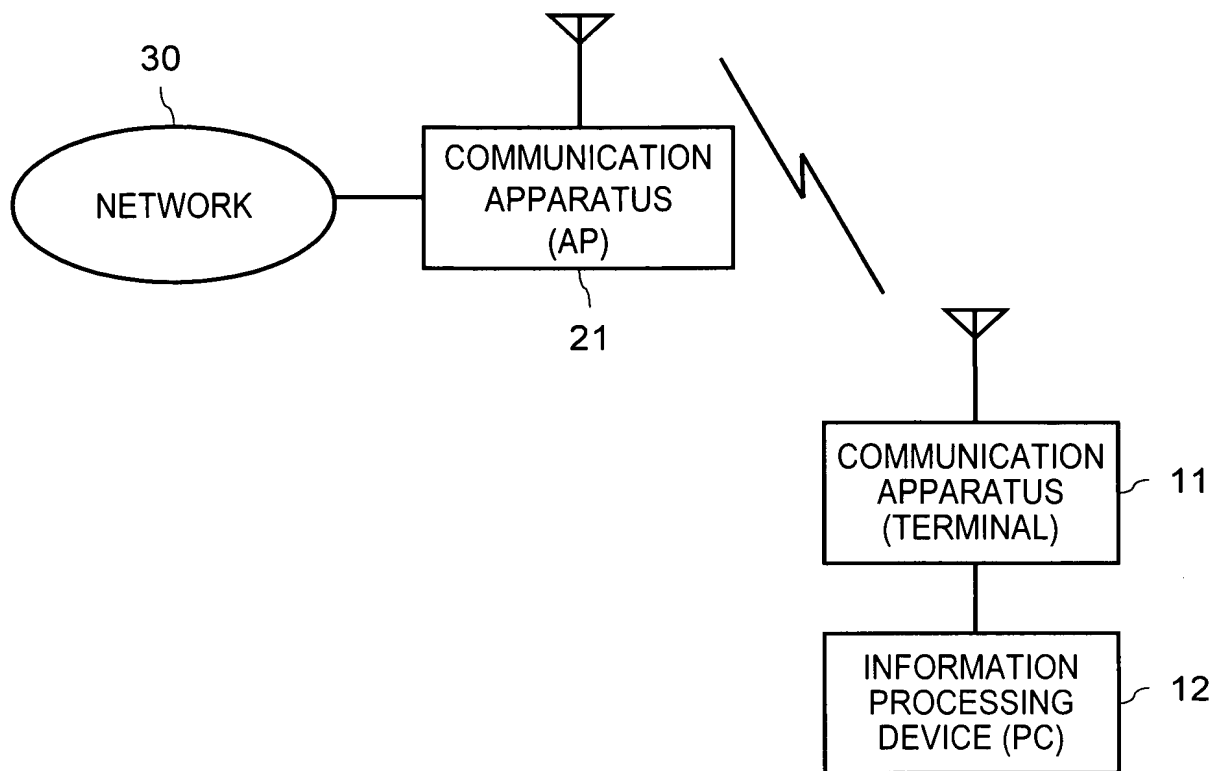


FIG. 2

2/32

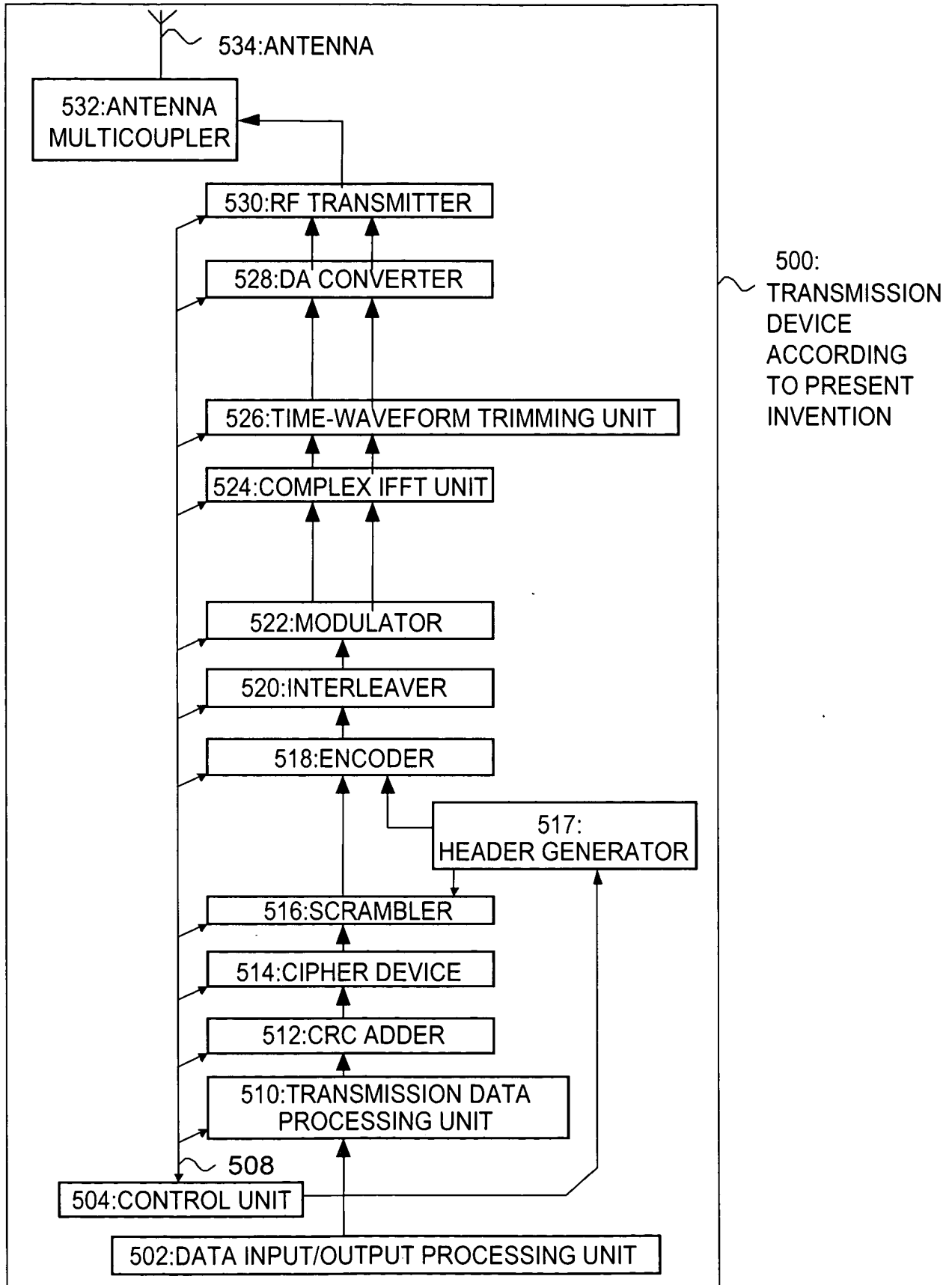


FIG. 3

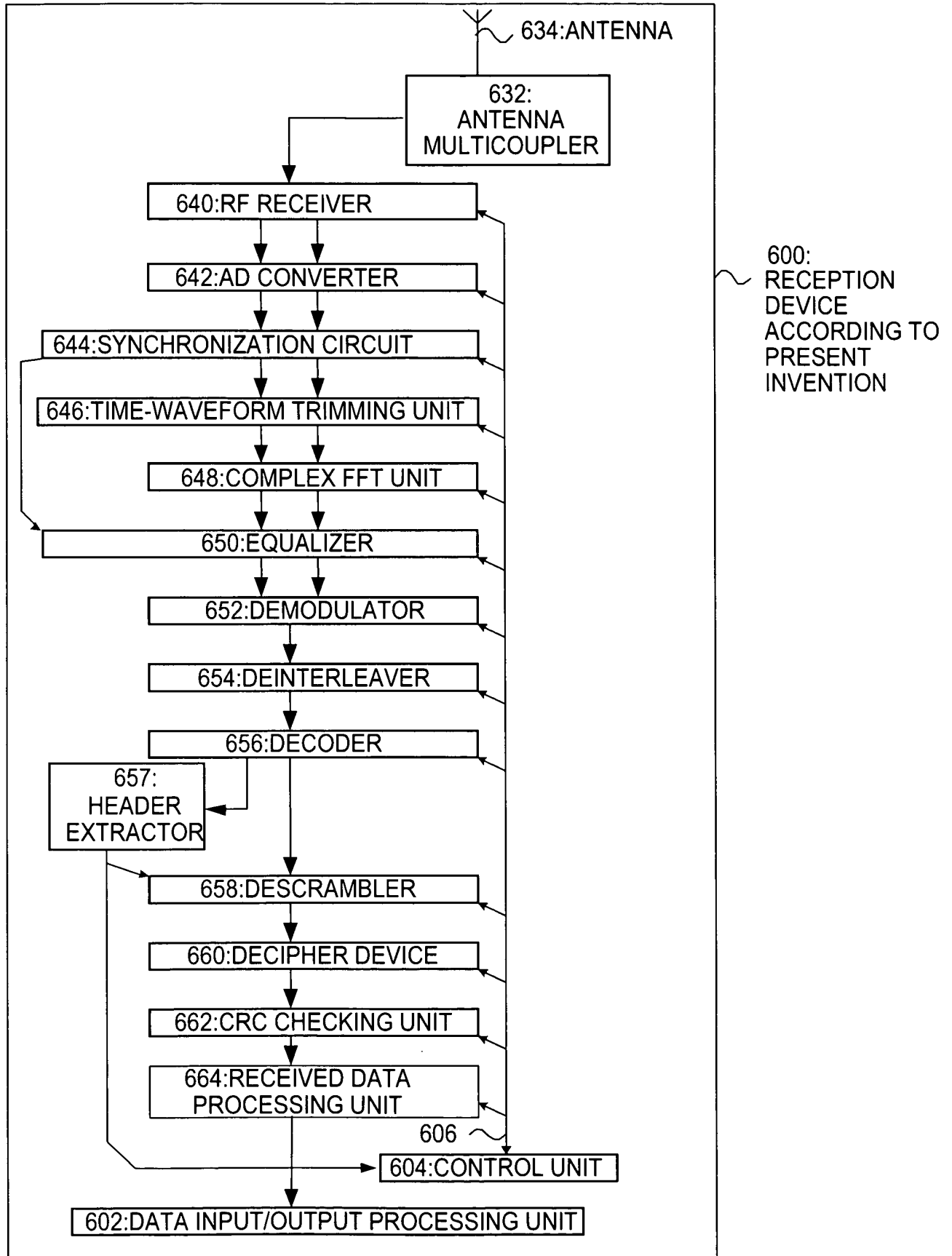


FIG. 4

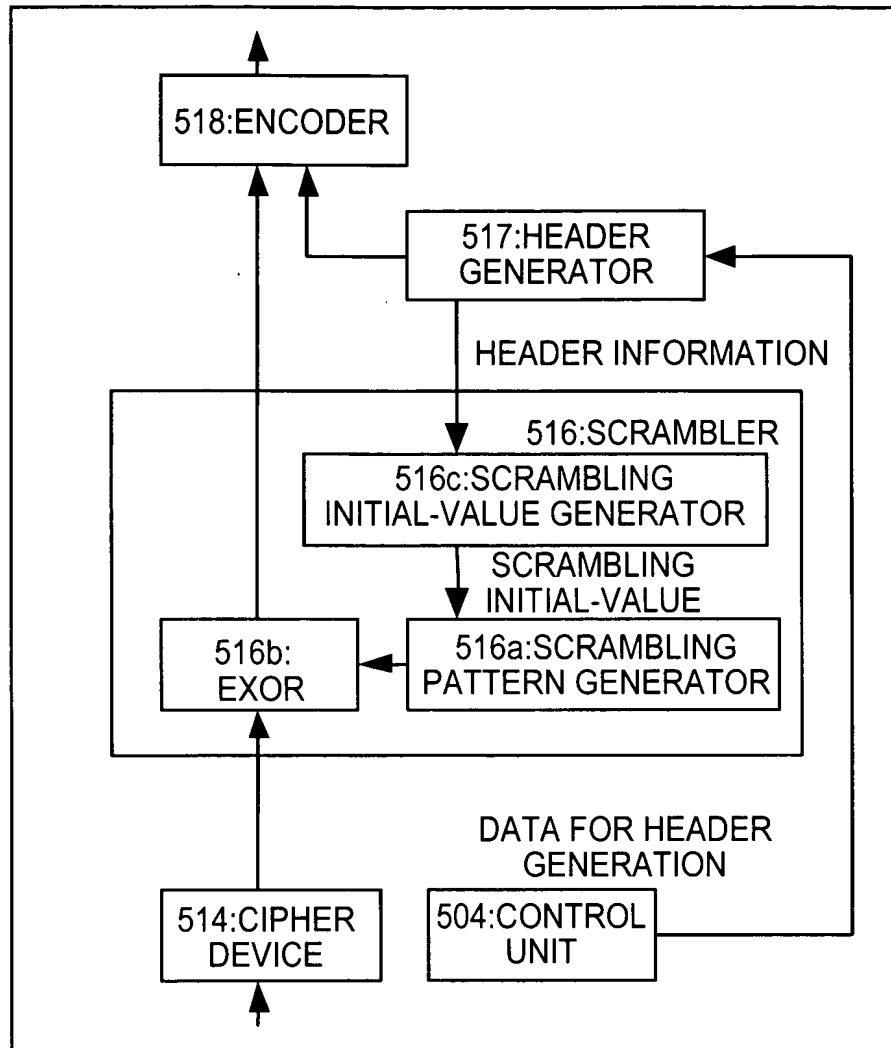


FIG. 5

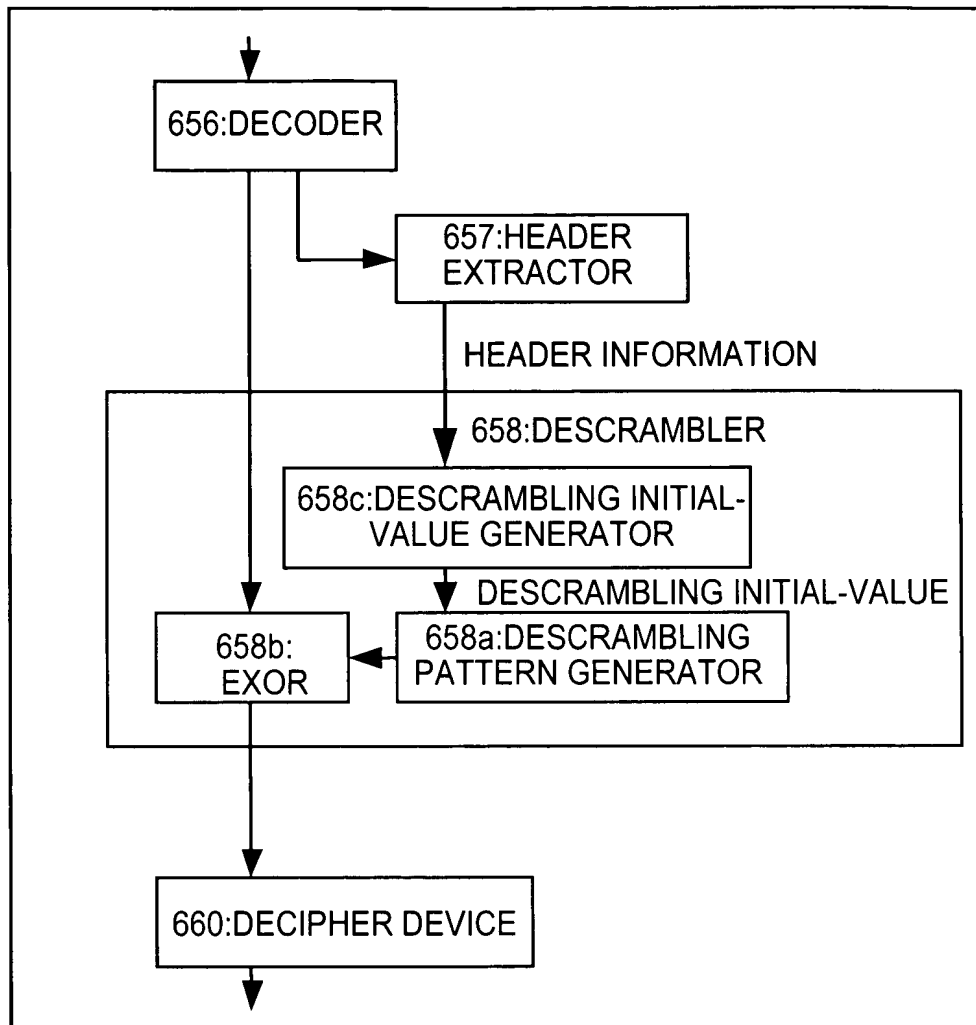


FIG. 6

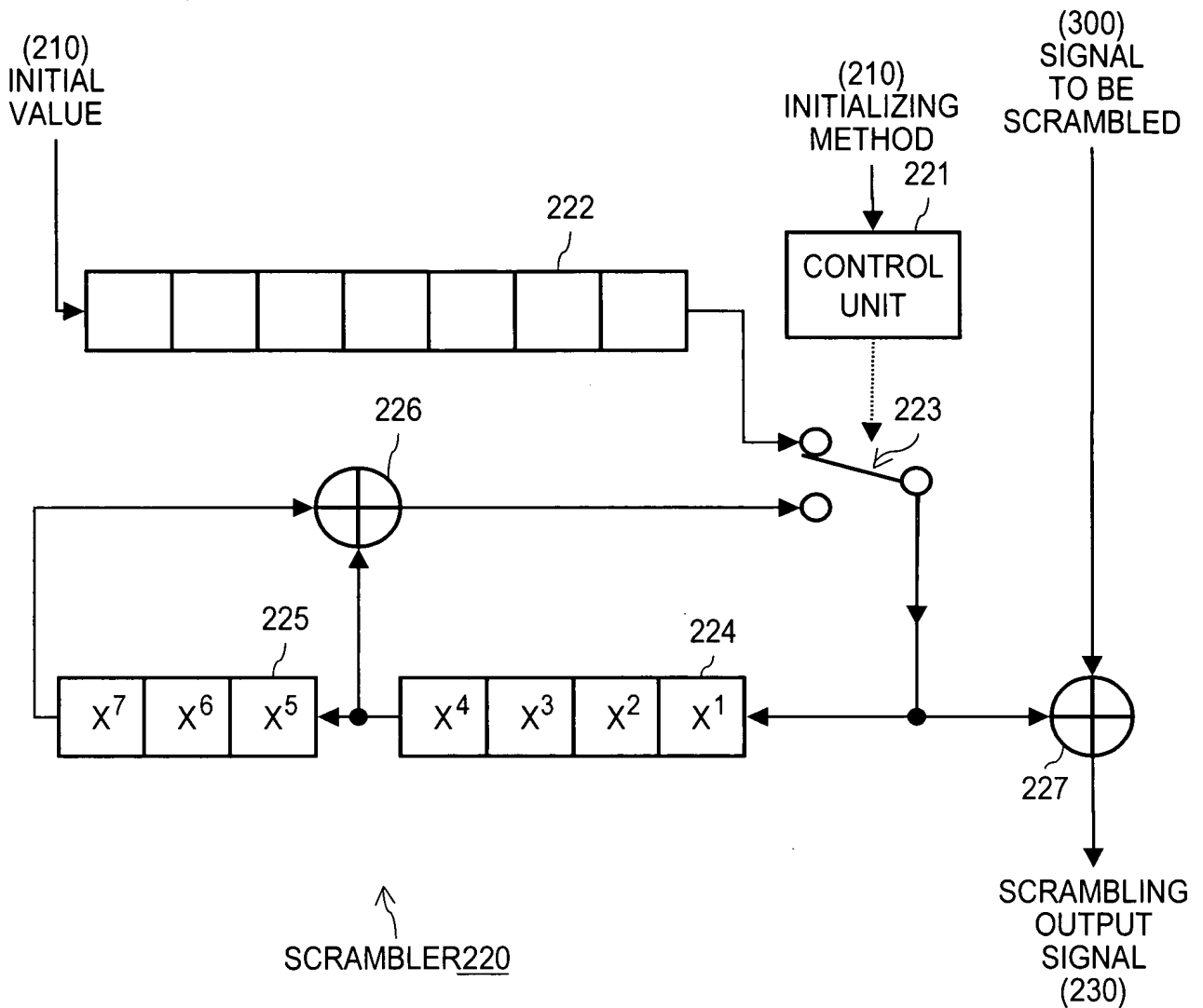


FIG. 7

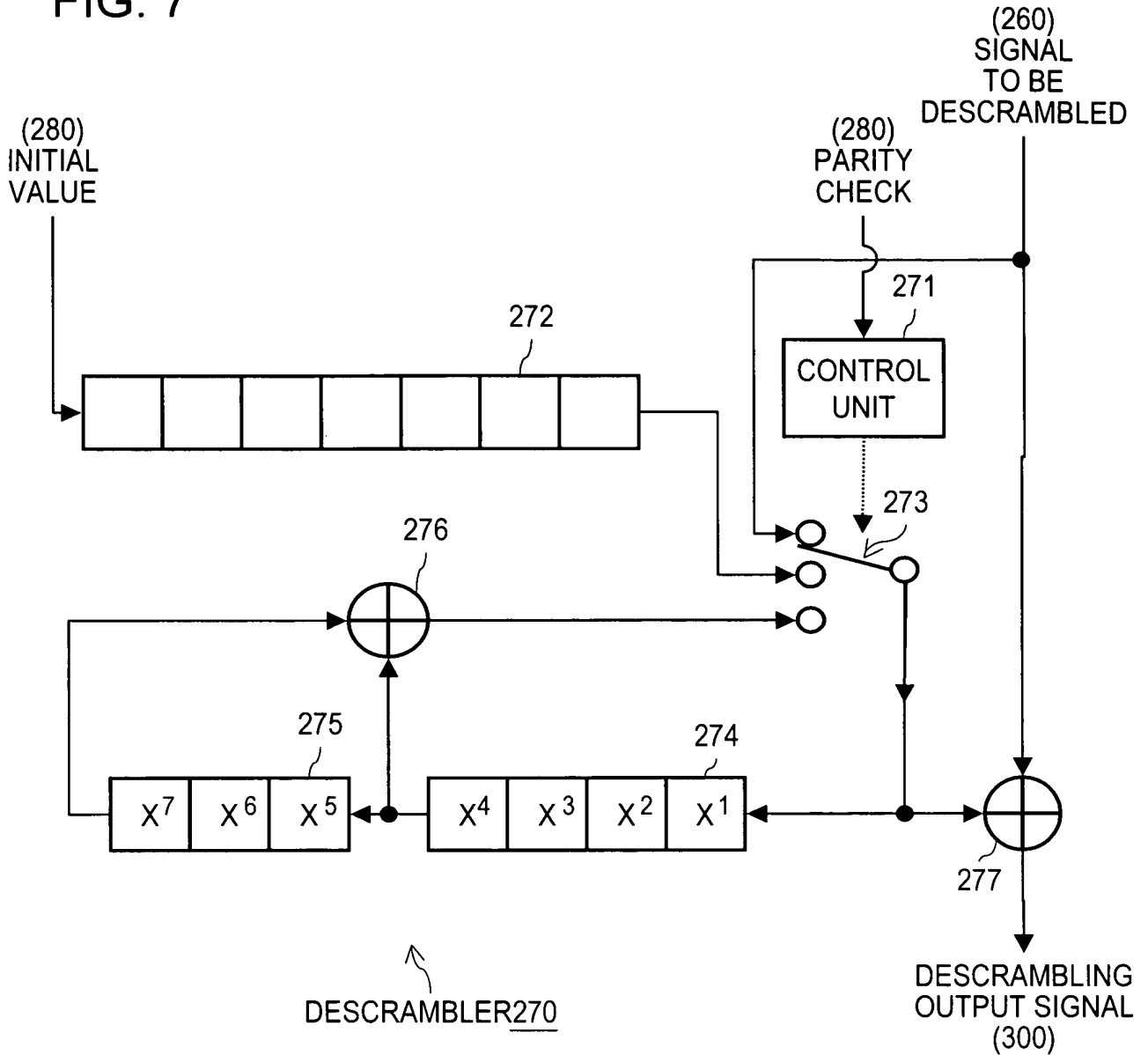


FIG. 8

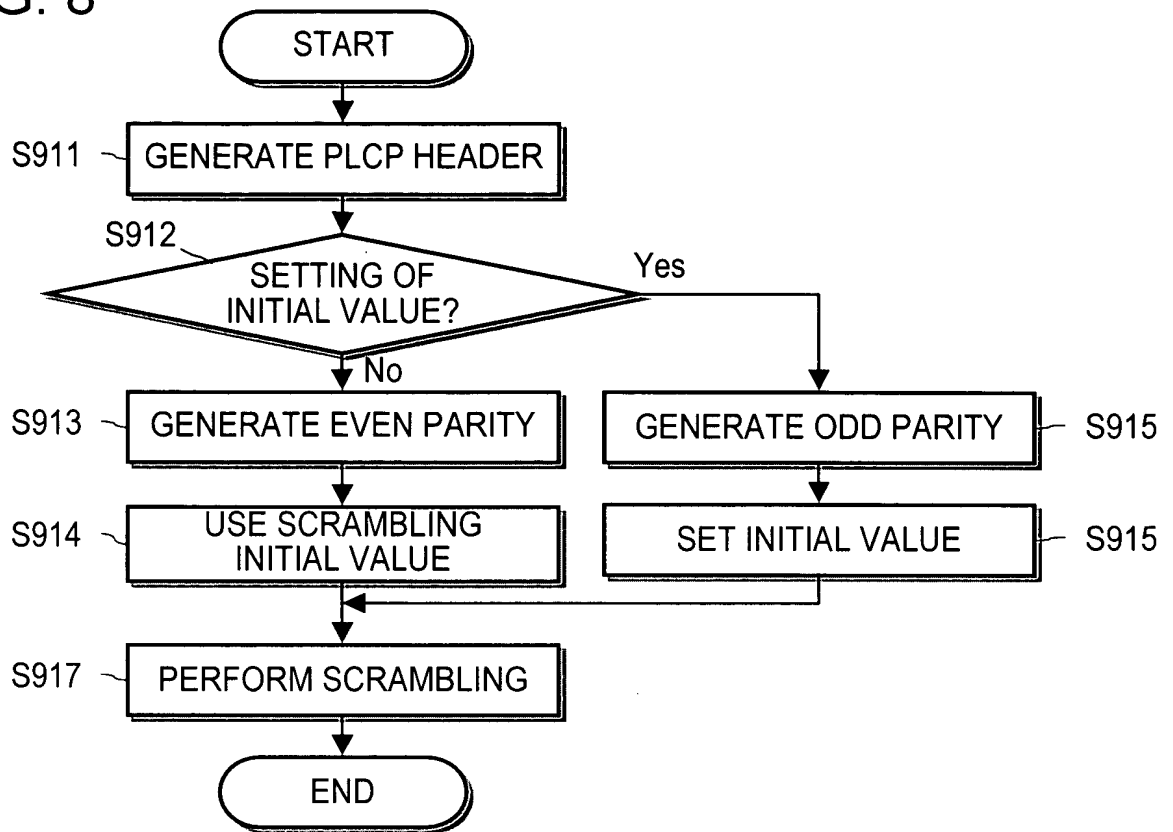


FIG. 9

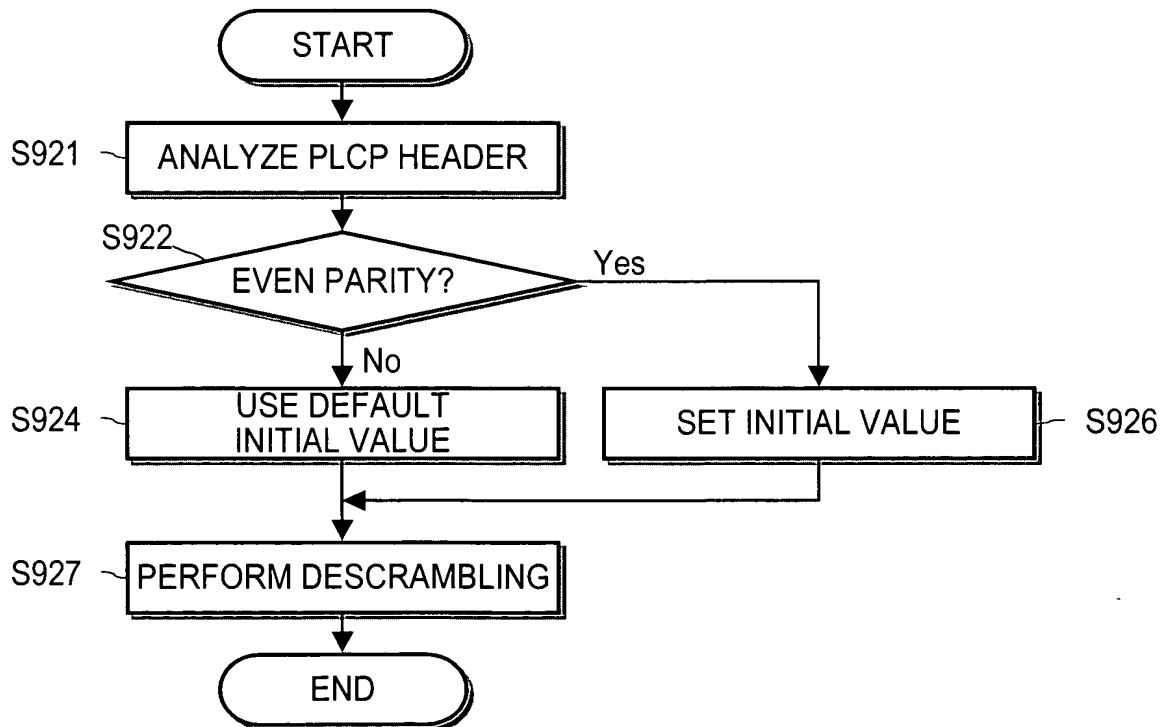
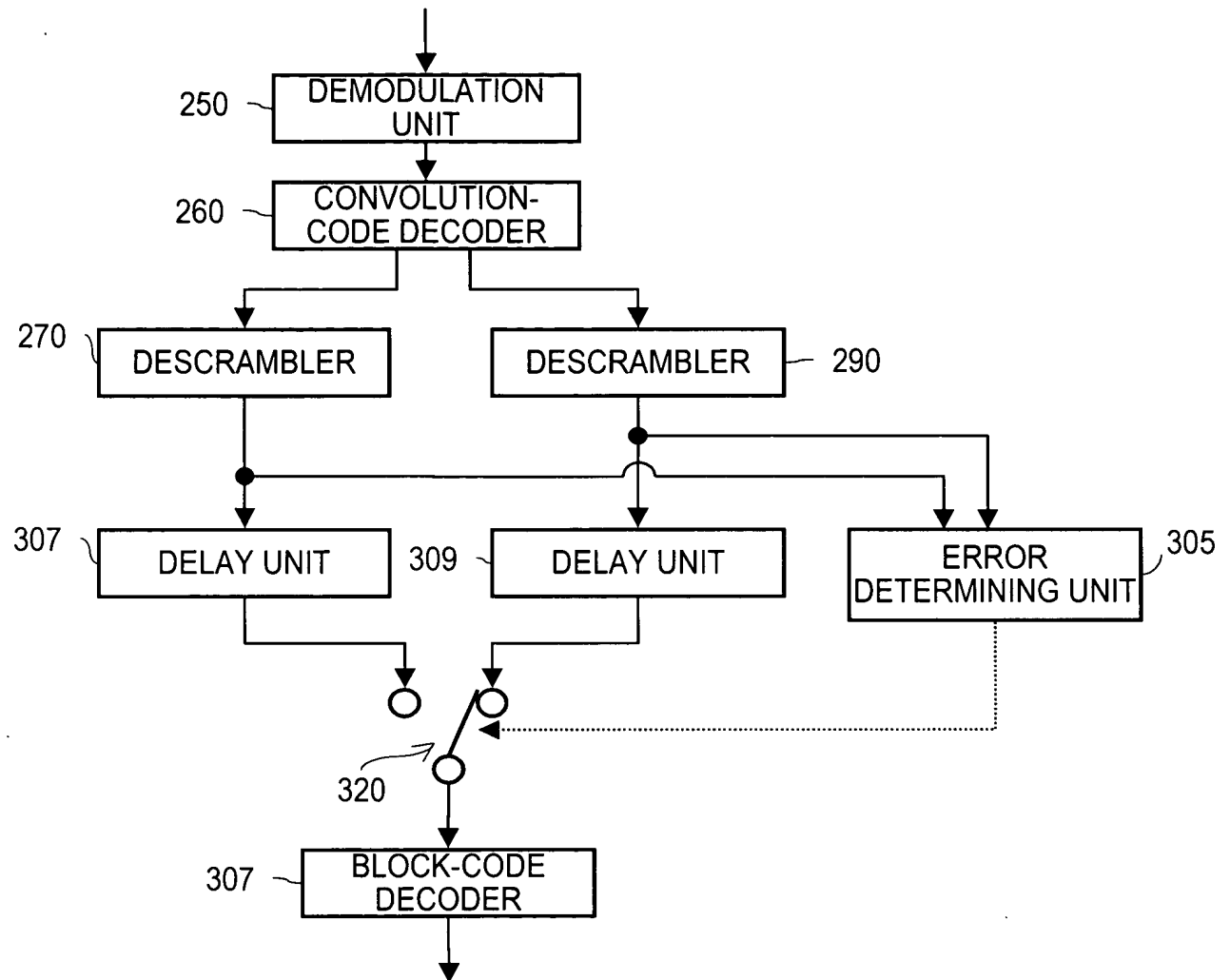




FIG. 10



10/32

S04P0975

FIG. 11

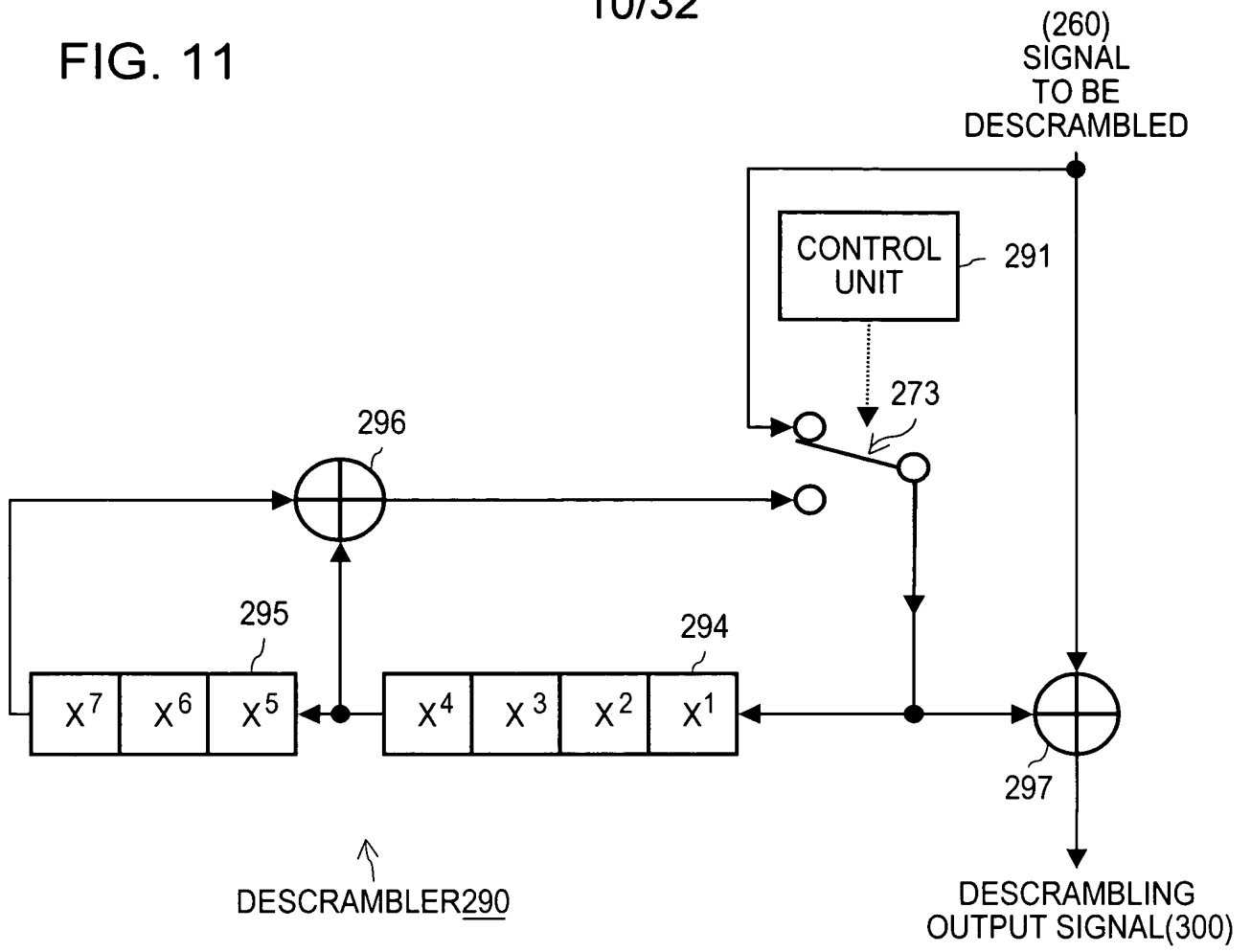


FIG. 12

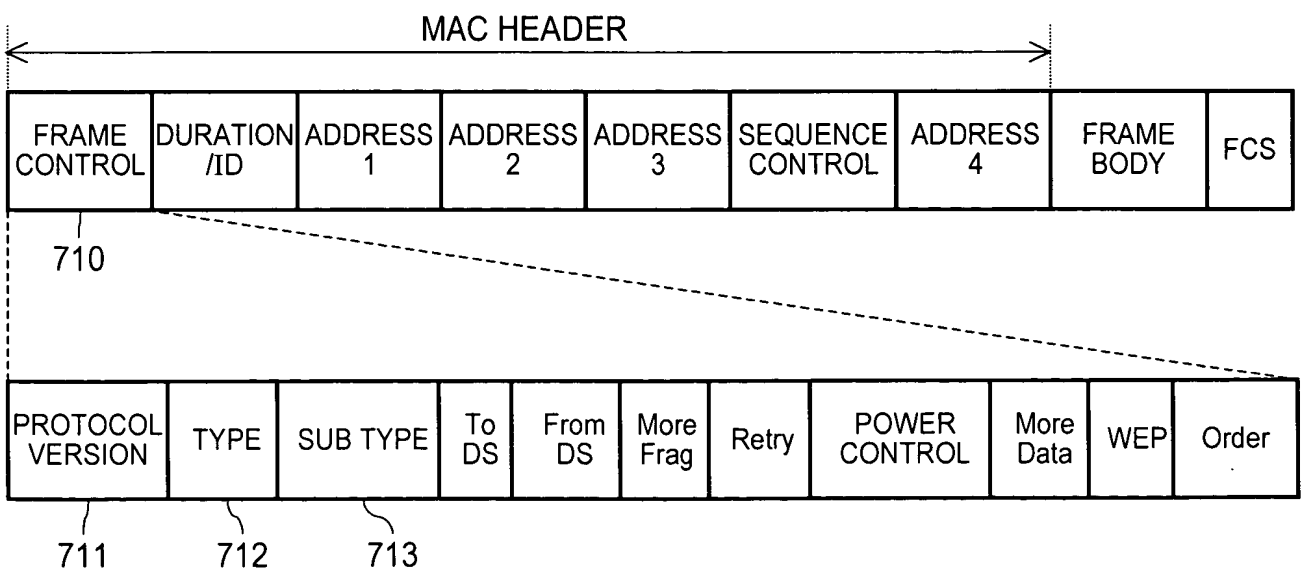


FIG. 13

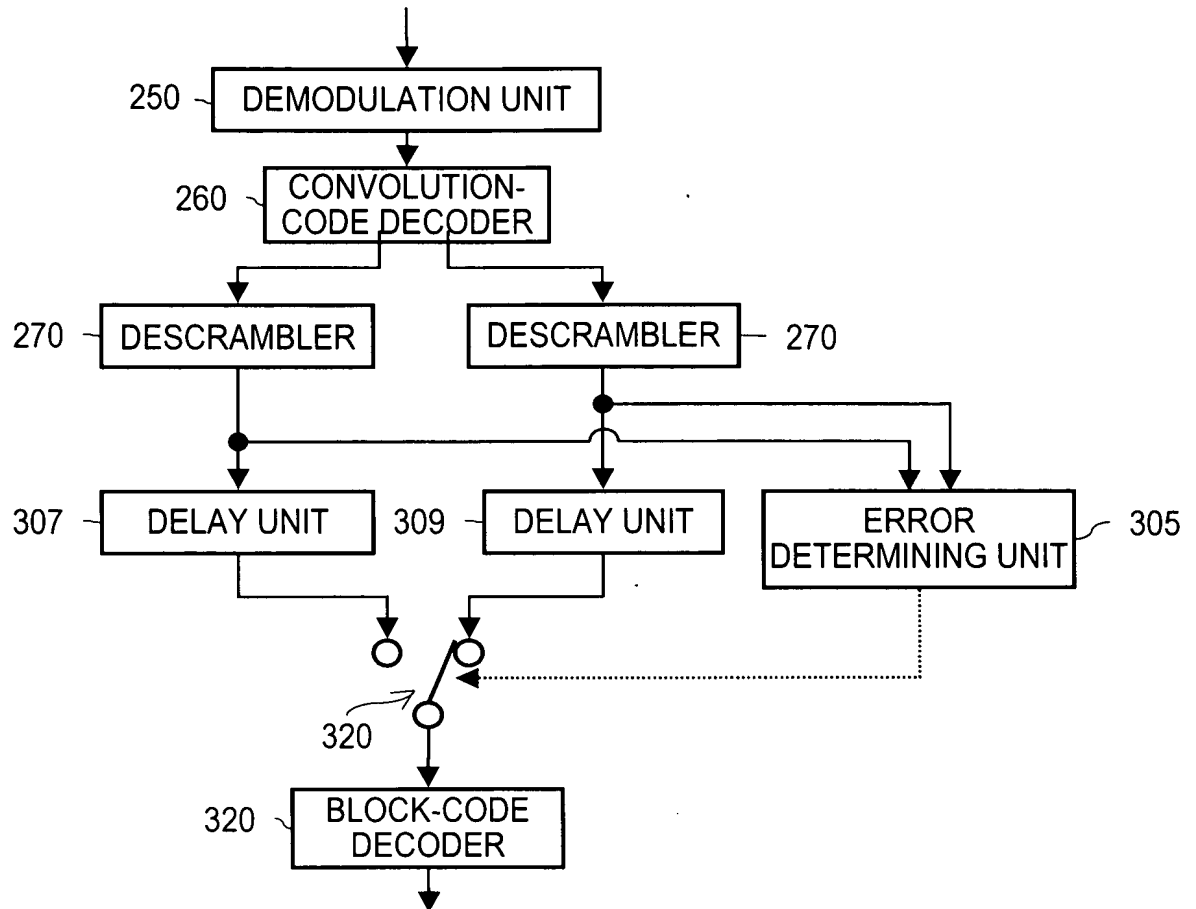


FIG. 14

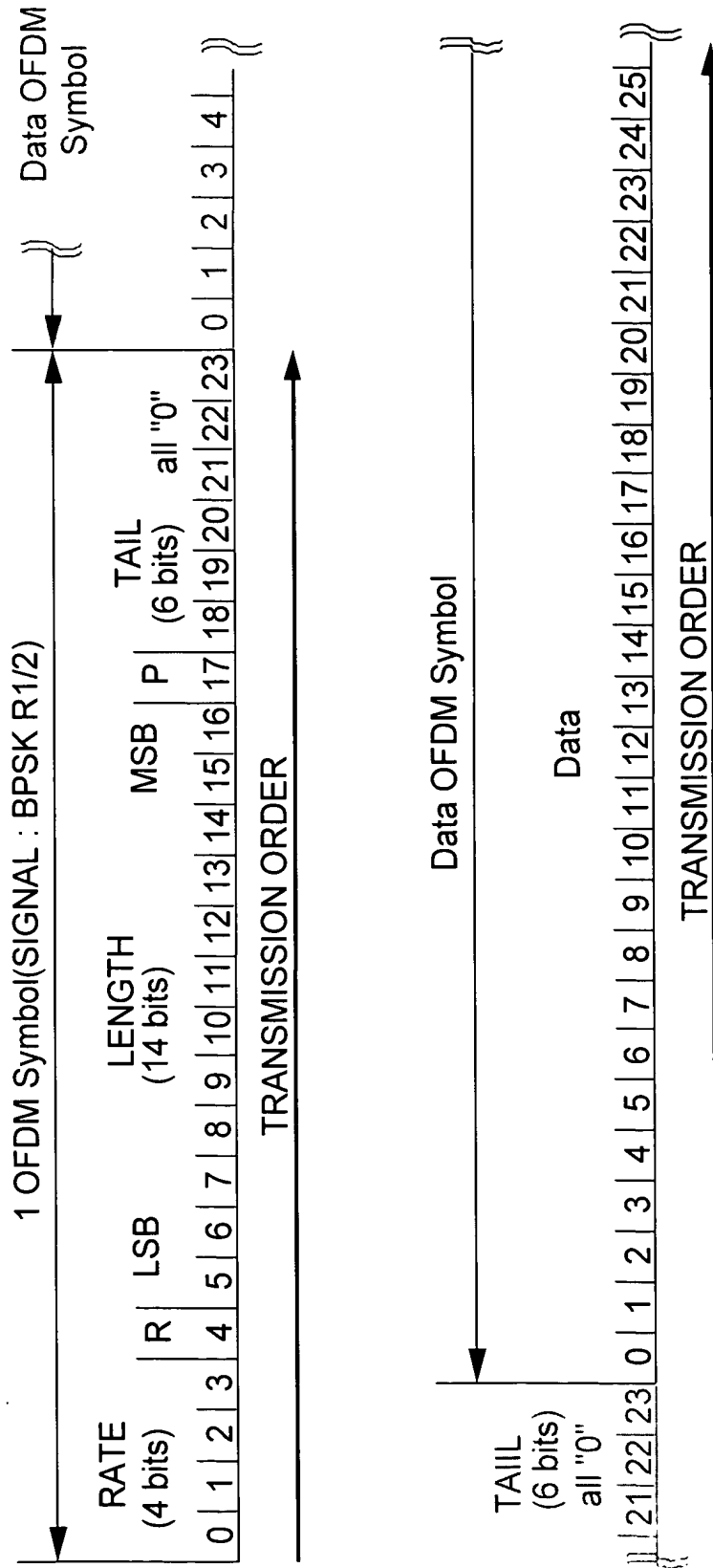


FIG. 15

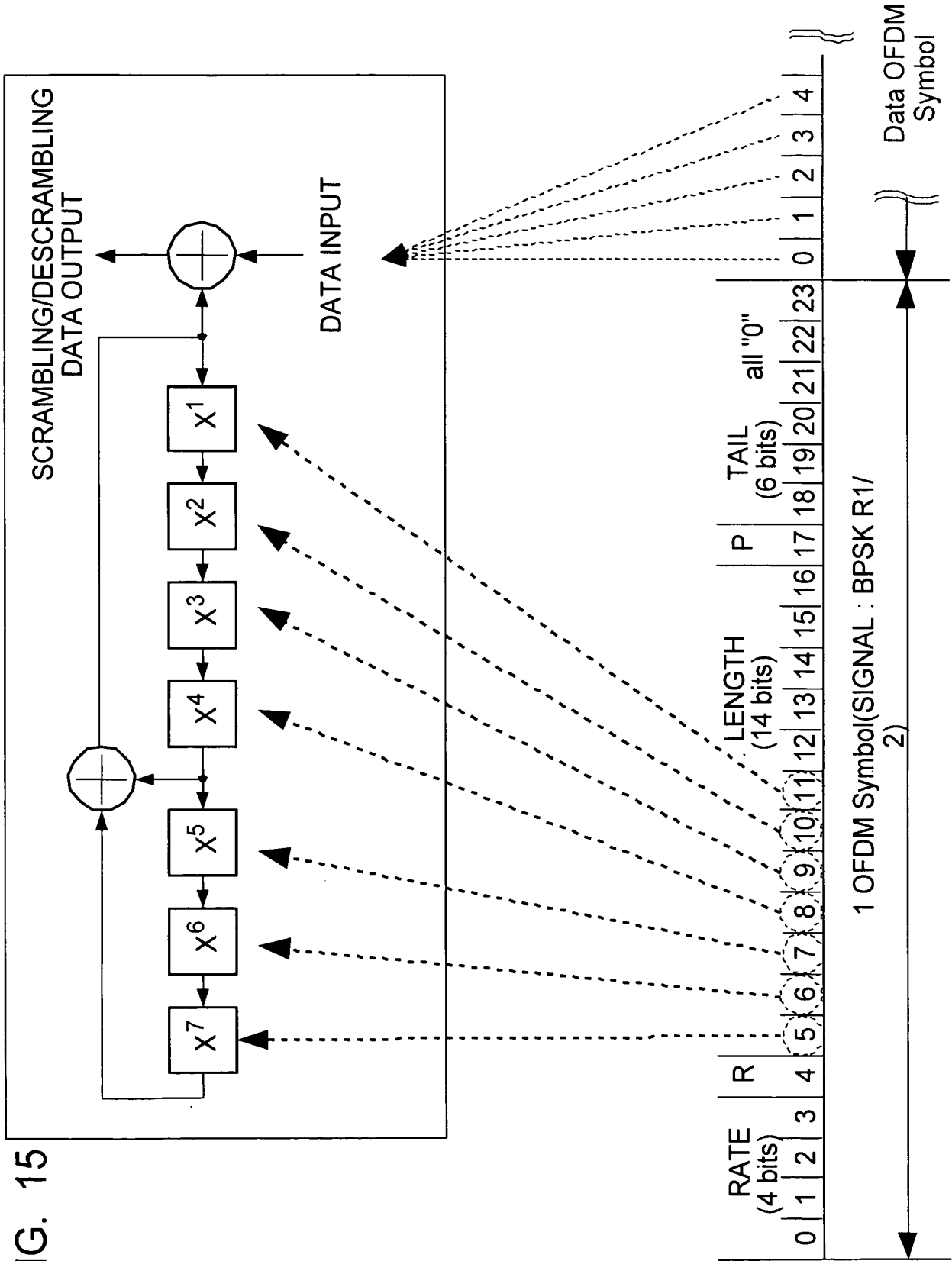


FIG. 16

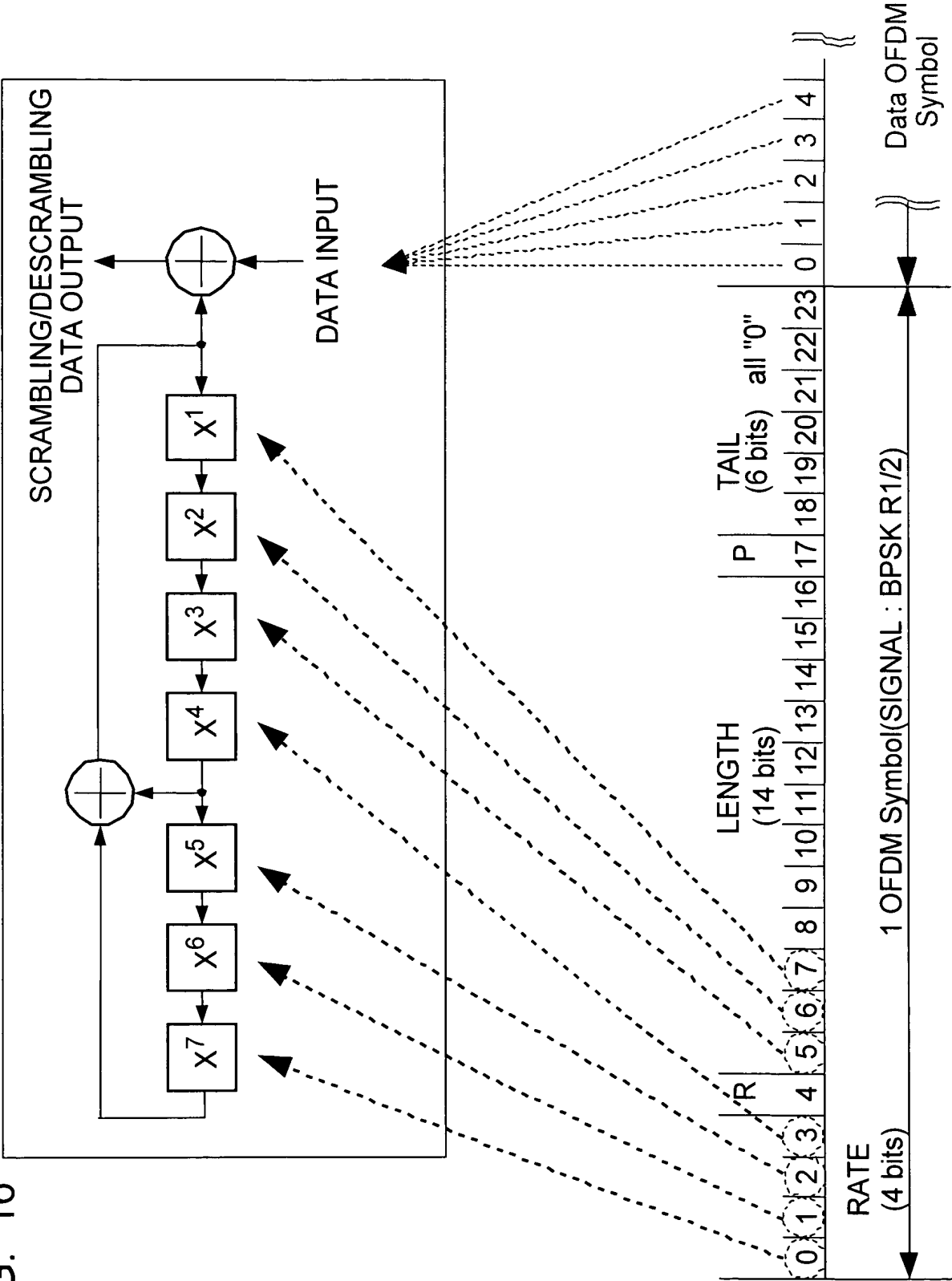
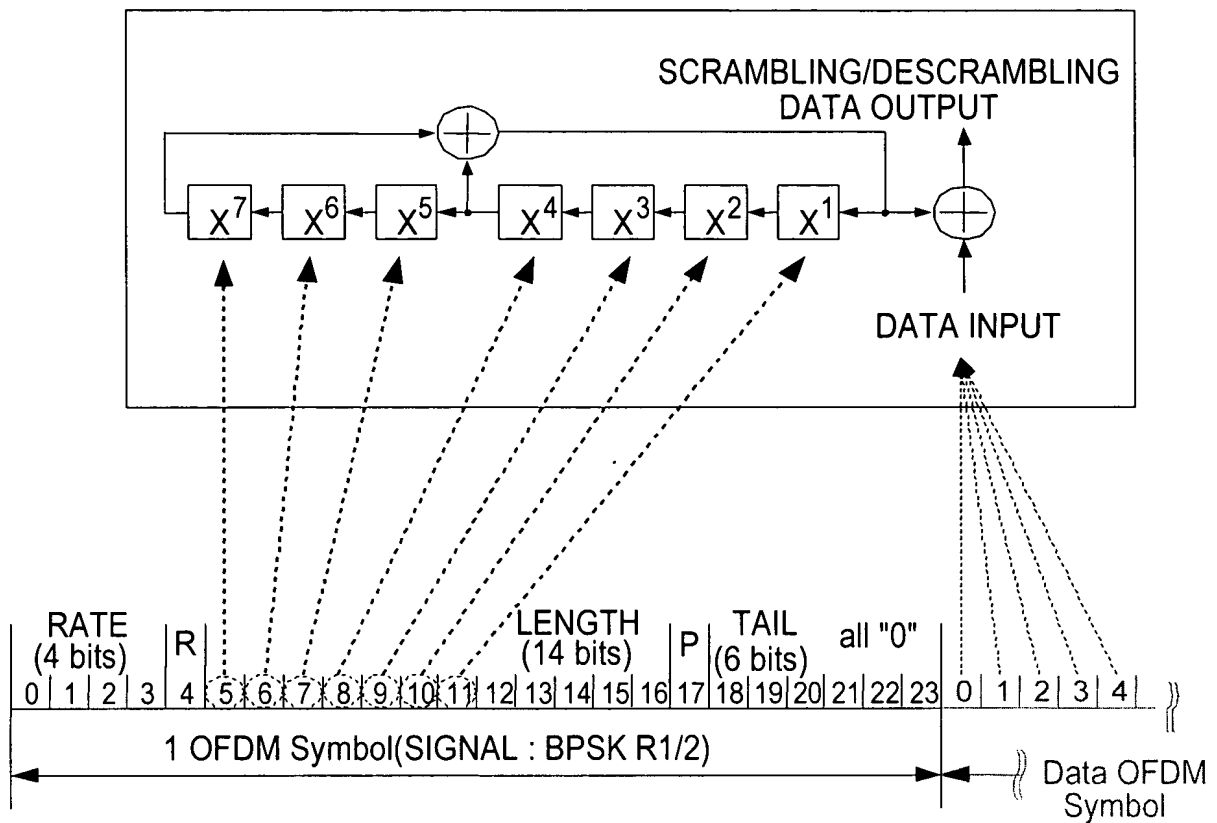
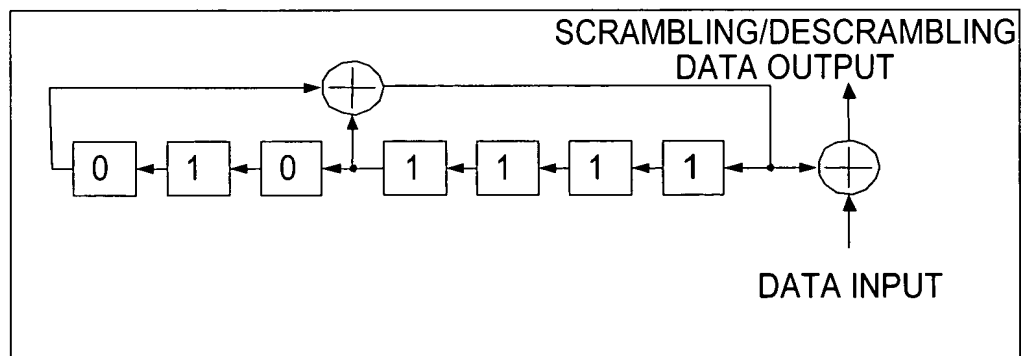


FIG. 17



(a) CASE THAT EXTRACTED BITS ARE NOT ALL ZERO



(b) CASE THAT EXTRACTED BITS ARE ALL ZERO

FIG. 18

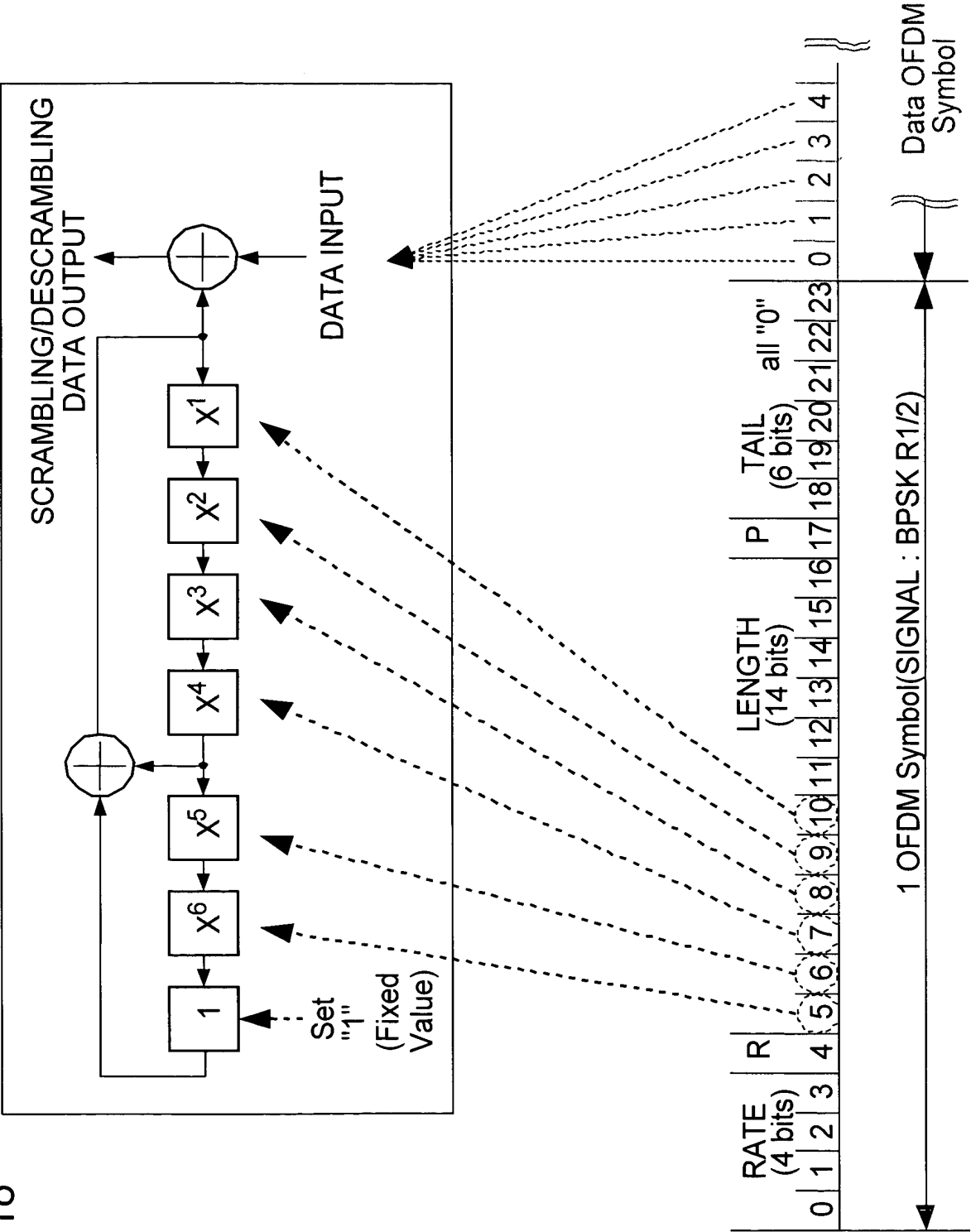




FIG. 19

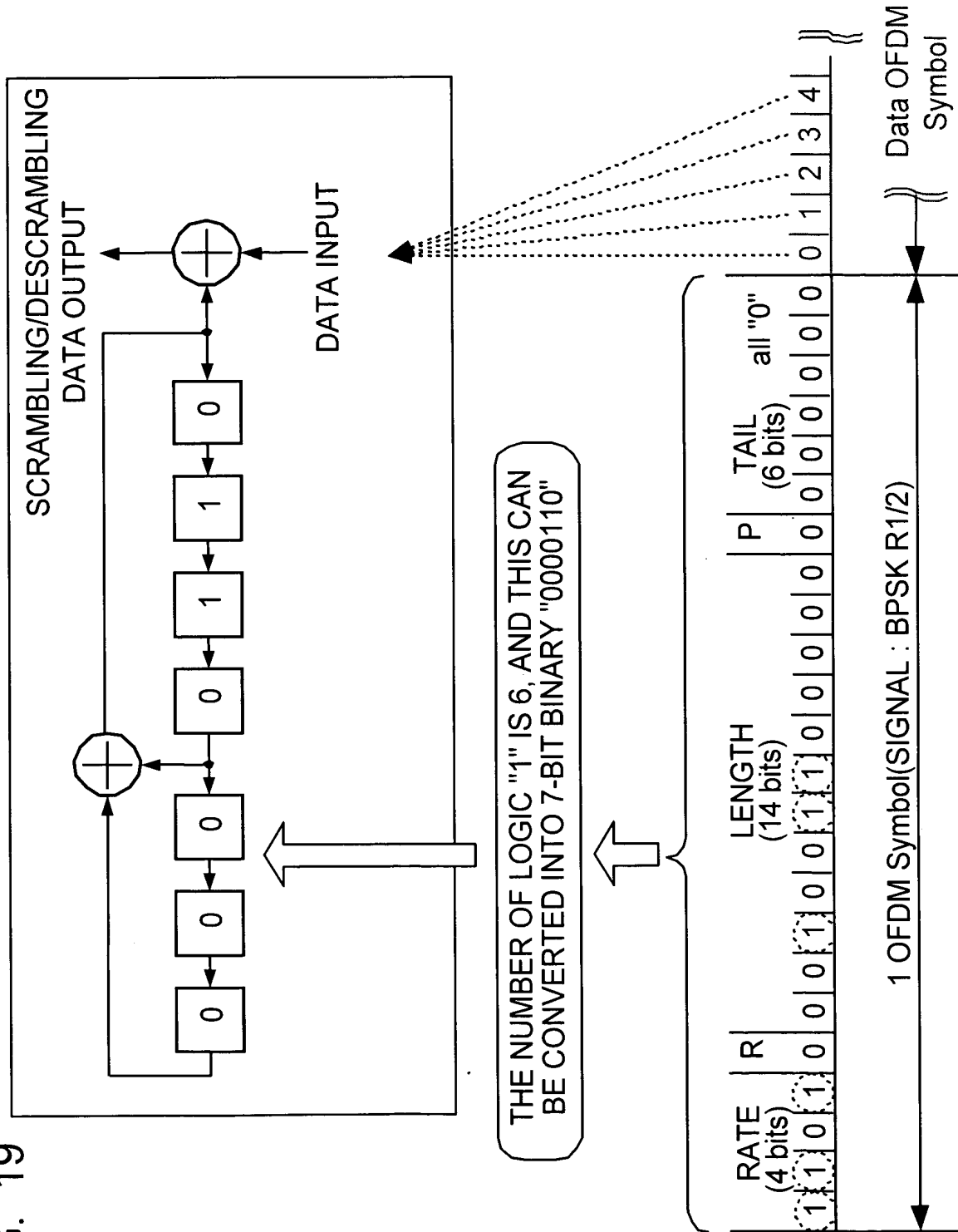
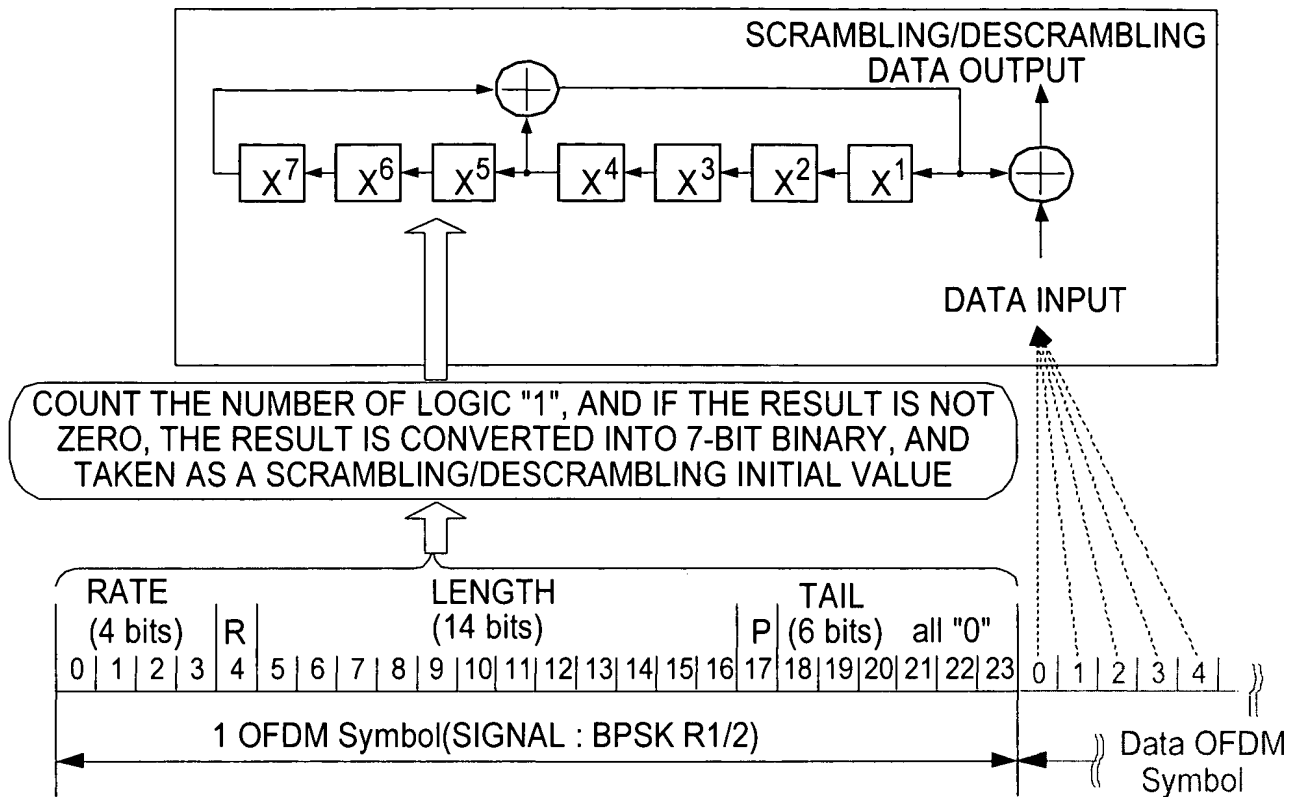
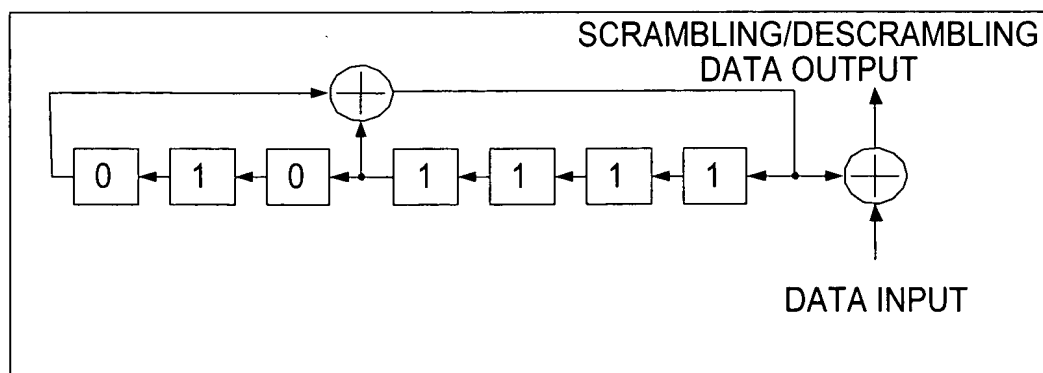


FIG. 20



(a) CASE THAT THE NUMBER OF BITS HAVING LOGIC "1" IS NOT ZERO



(b) CASE THAT THE NUMBER OF BITS HAVING LOGIC "1" IS ZERO

FIG. 21

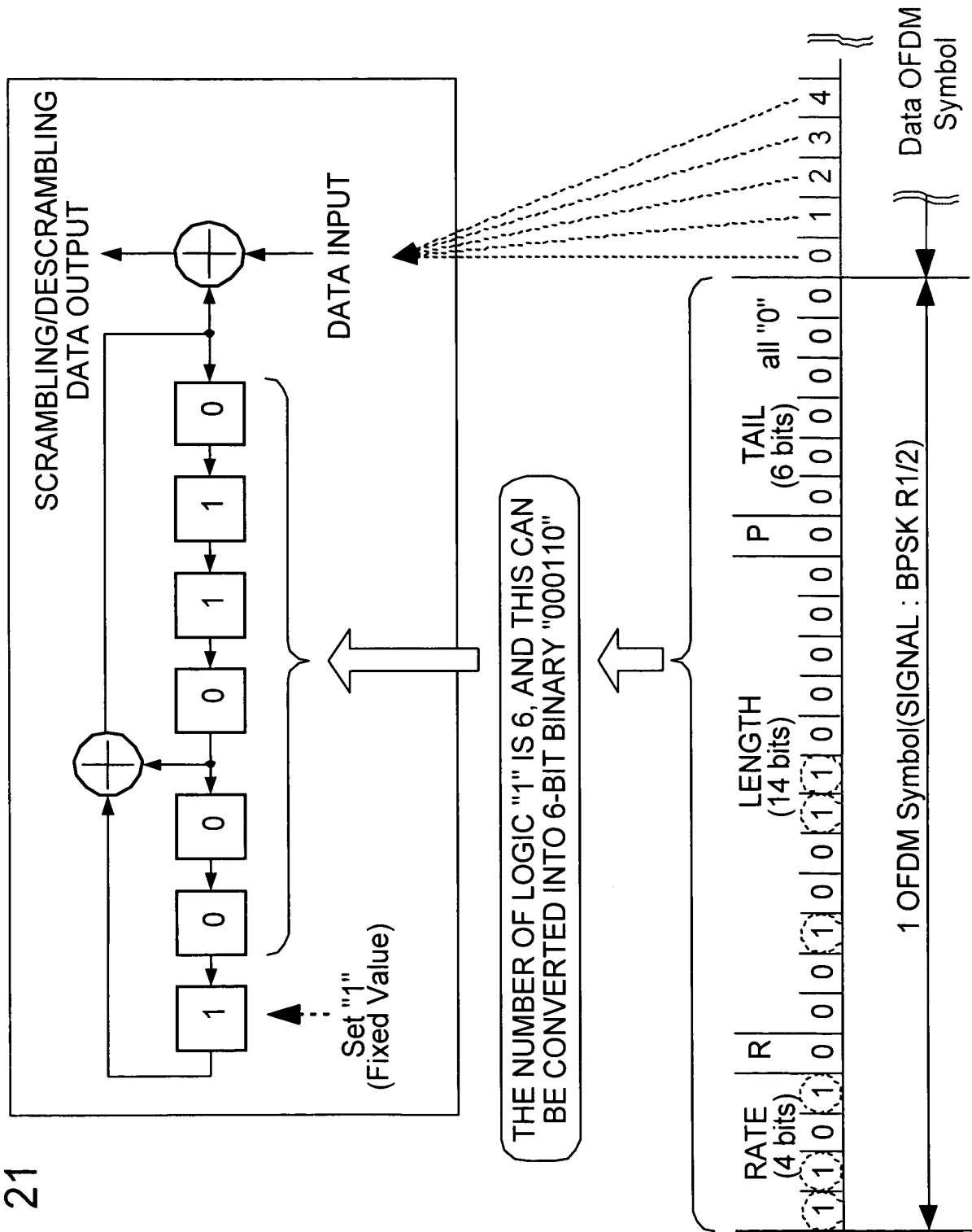
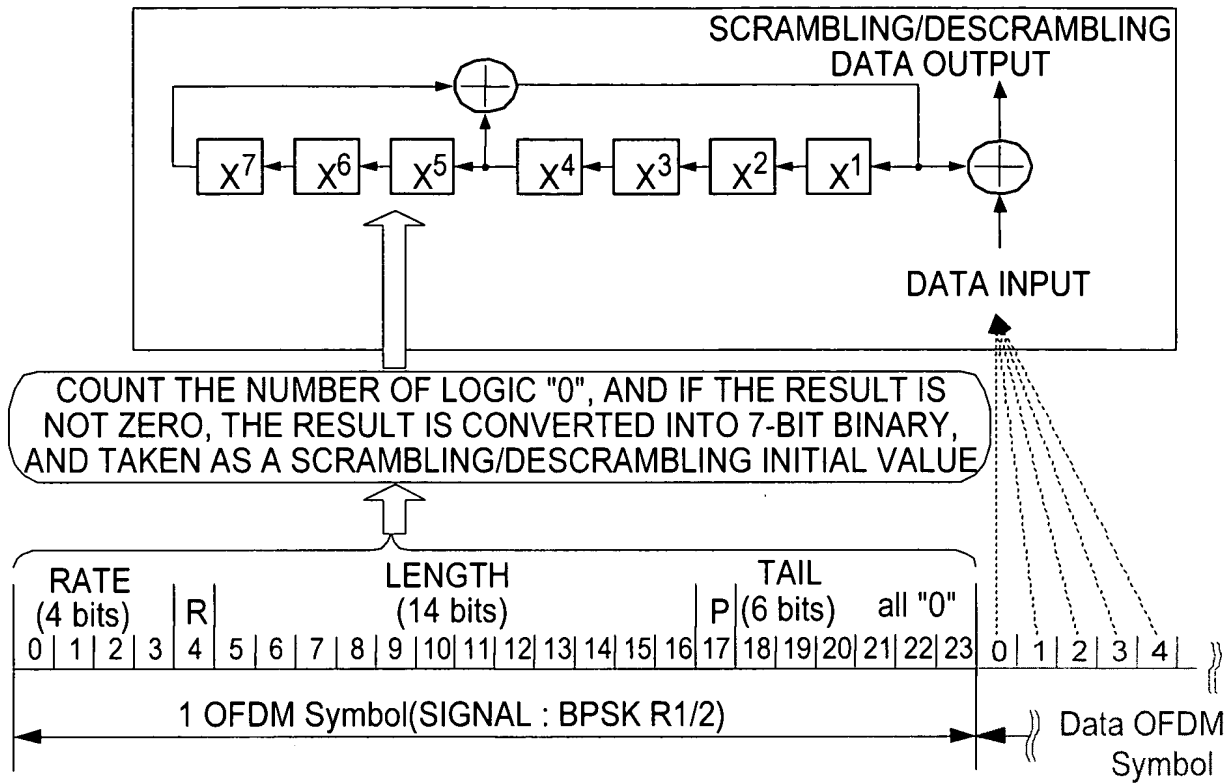


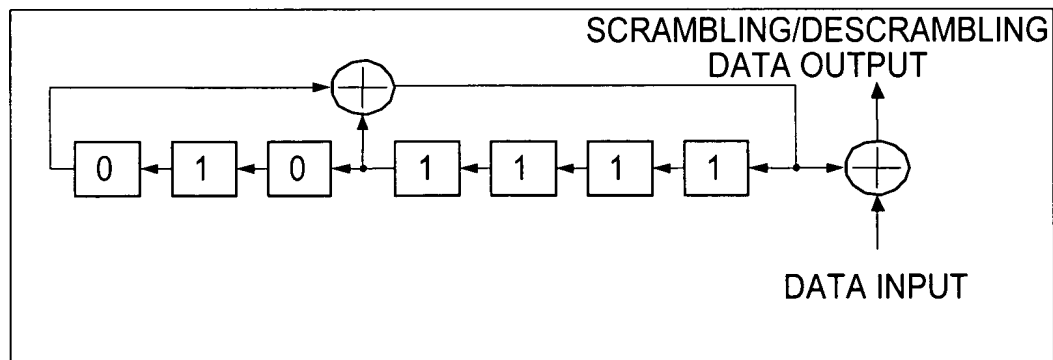




FIG. 24



(a) CASE THAT THE NUMBER OF BITS HAVING LOGIC "0" IS NOT ZERO



(b) CASE THAT THE NUMBER OF BITS HAVING LOGIC "0" IS ZERO

FIG. 25

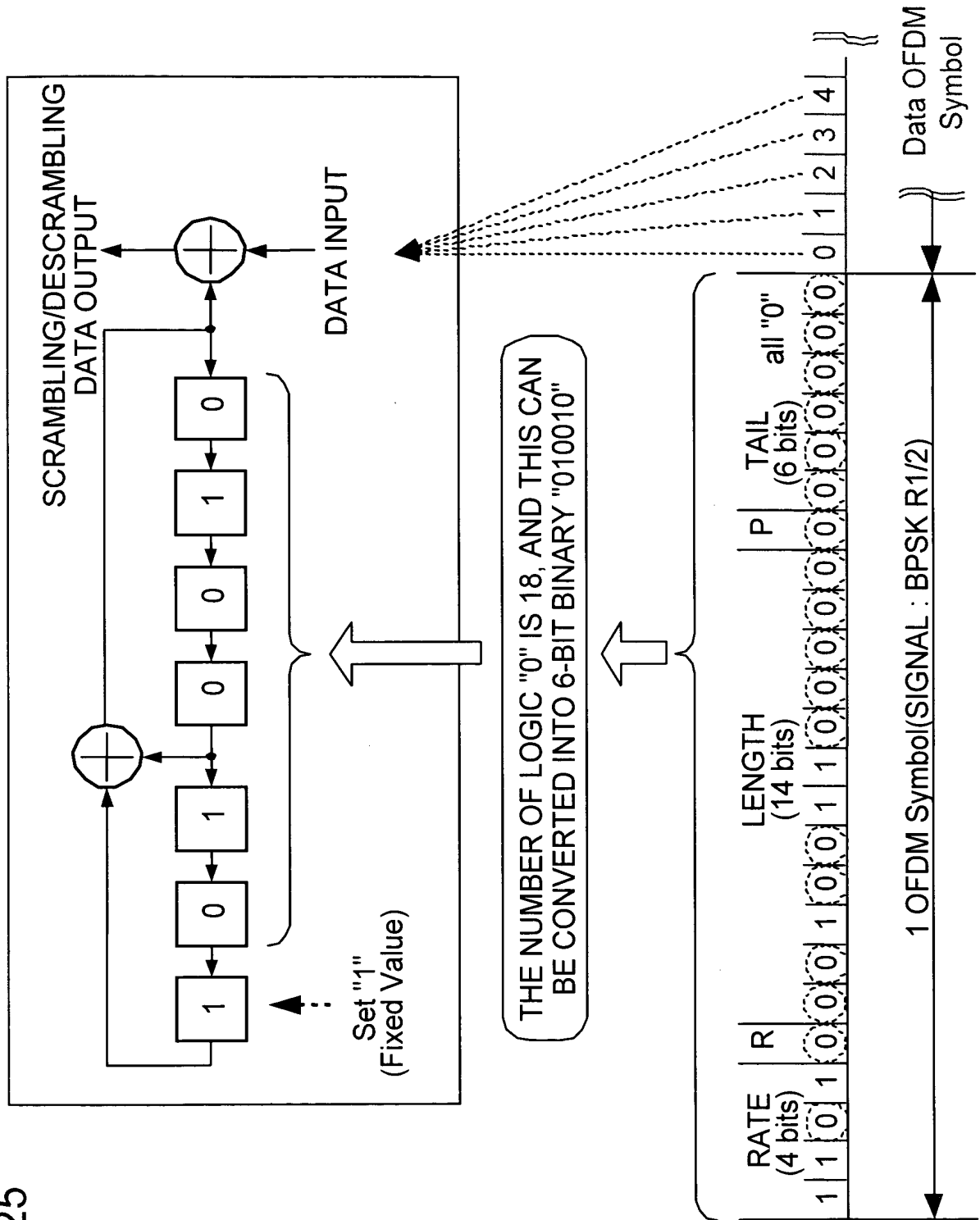


FIG. 26

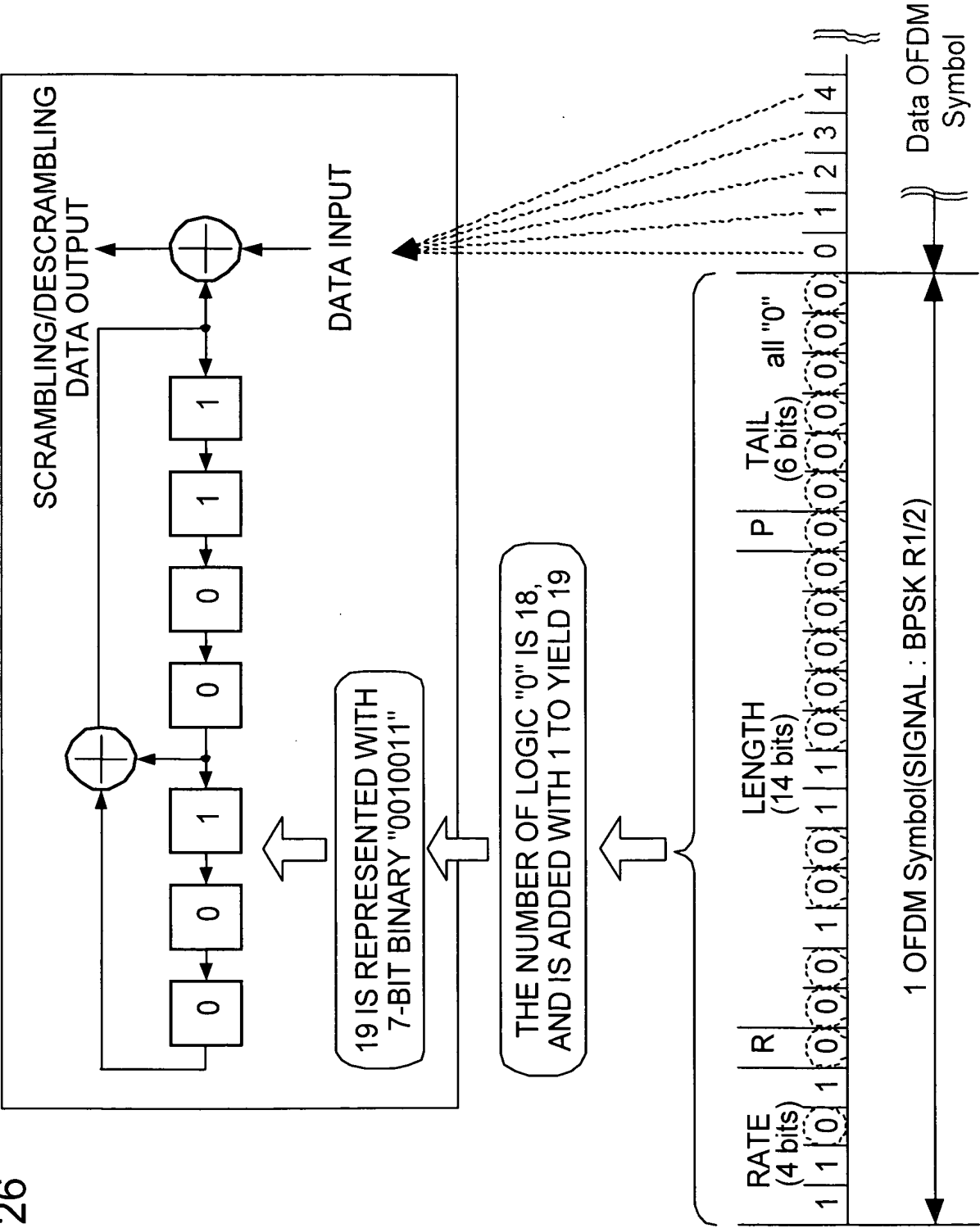




FIG. 27

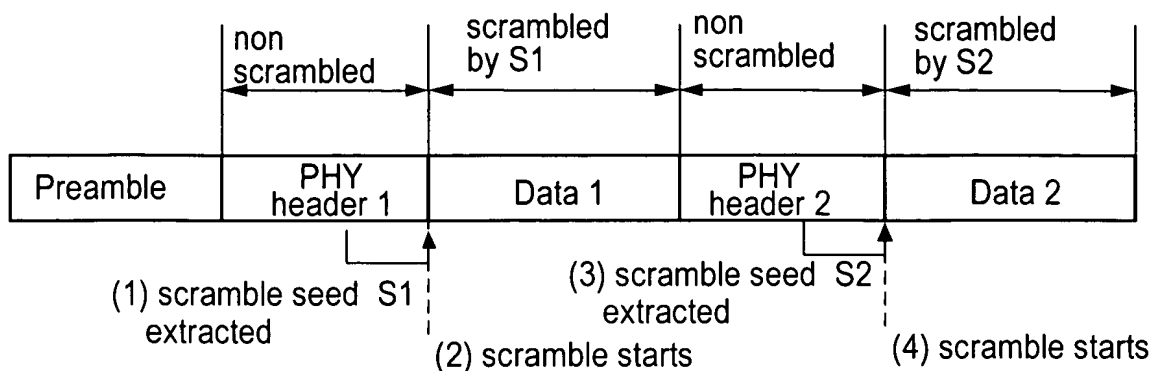


FIG. 28

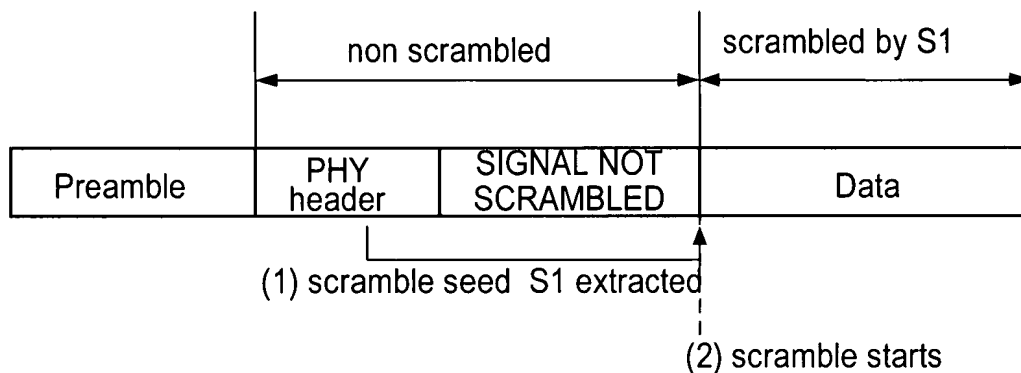


FIG. 29

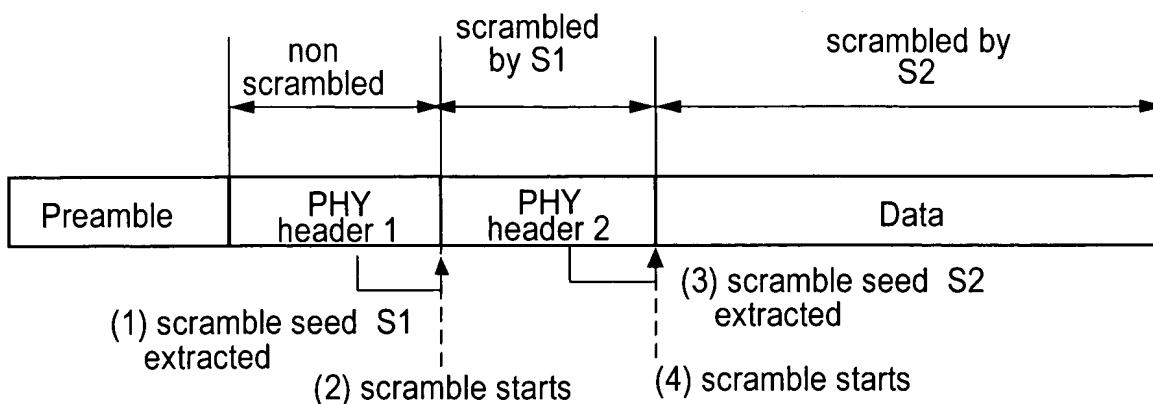


FIG. 30

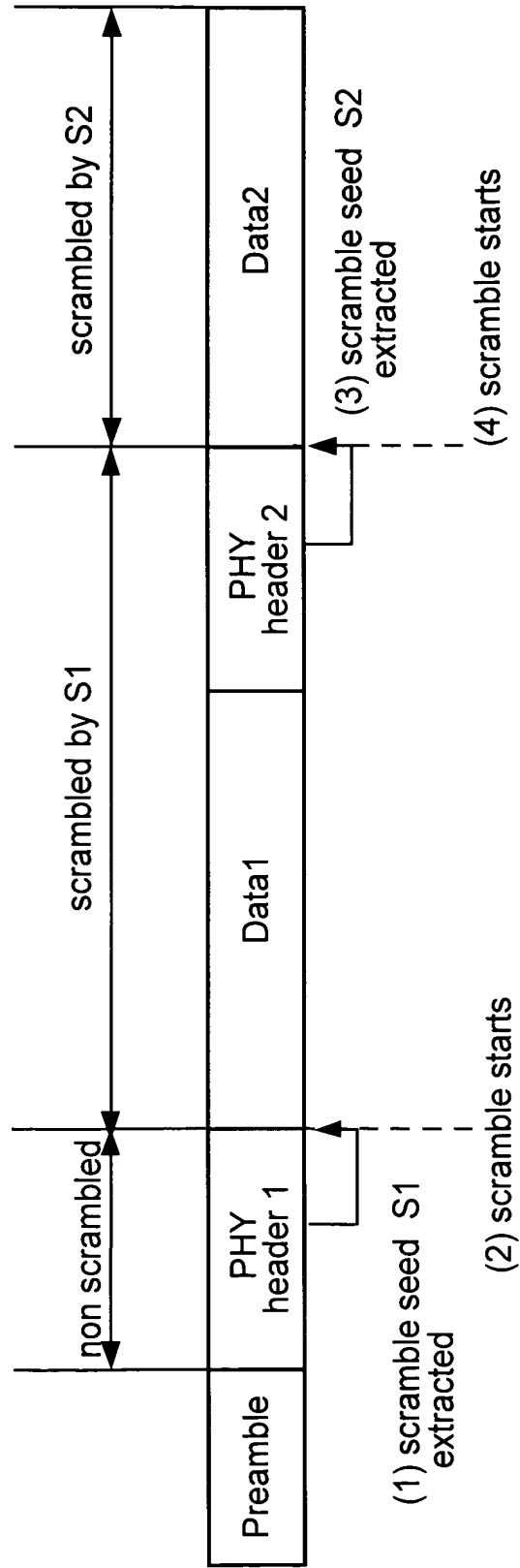


FIG. 31

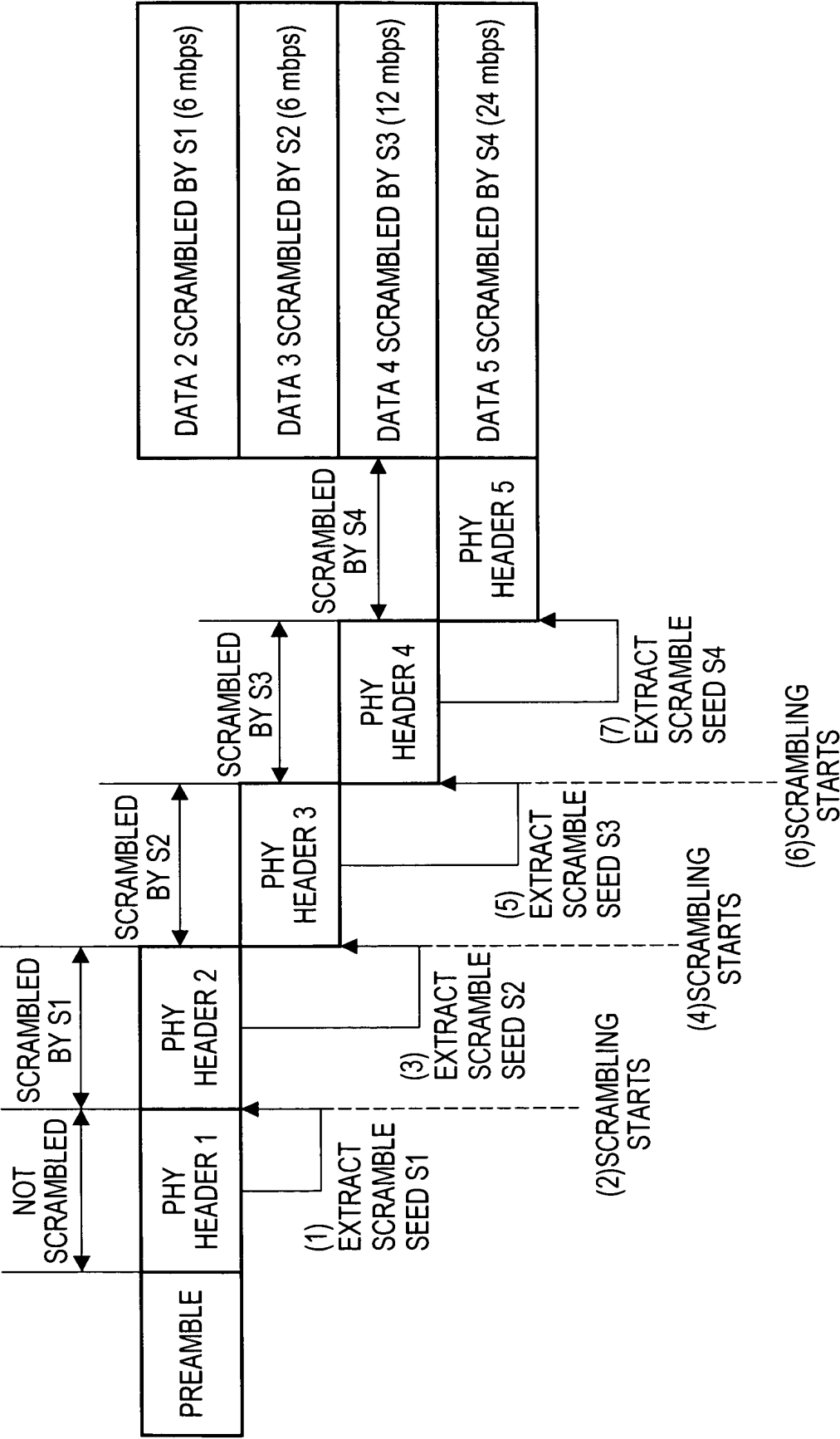


FIG. 32

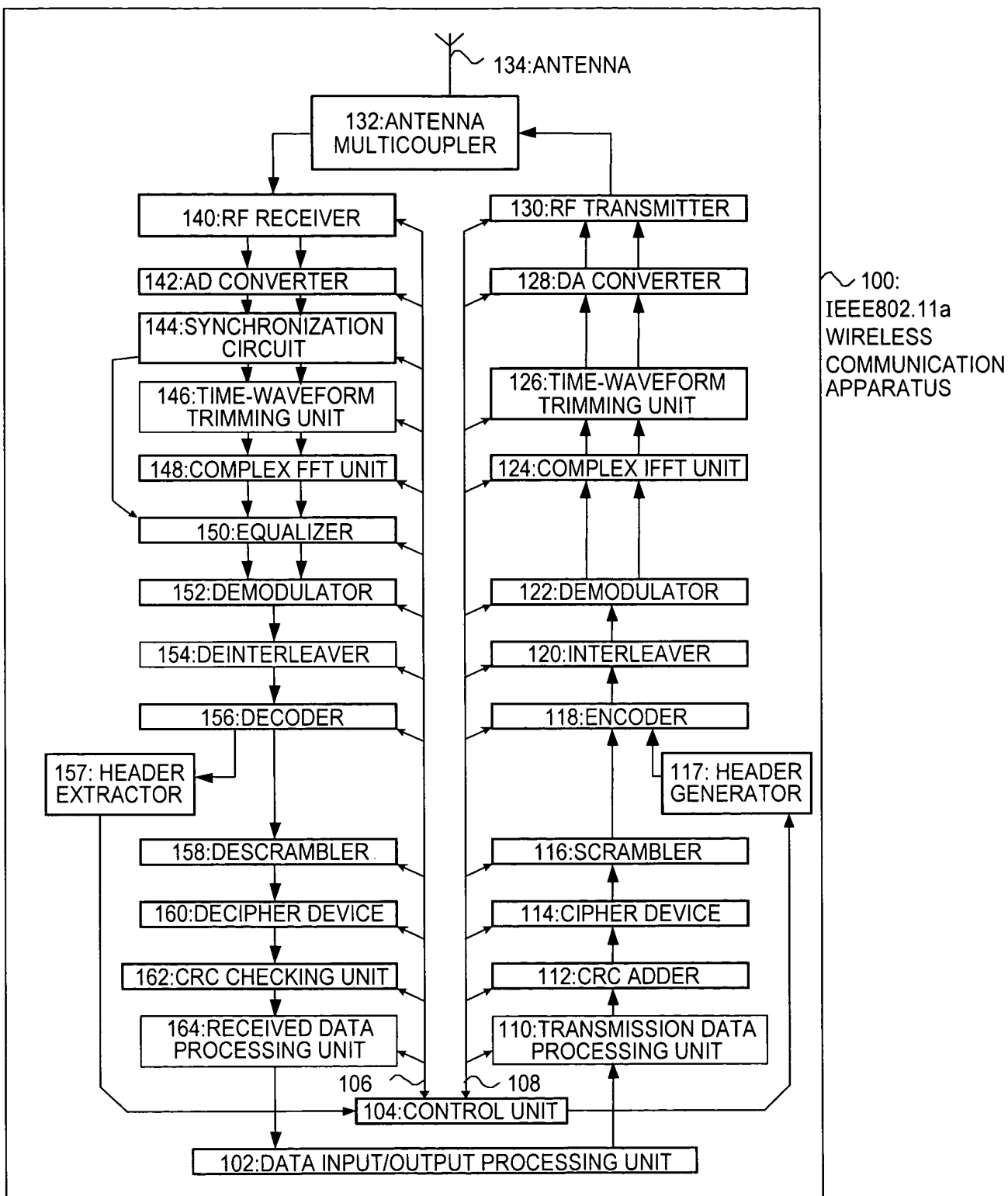


FIG. 33

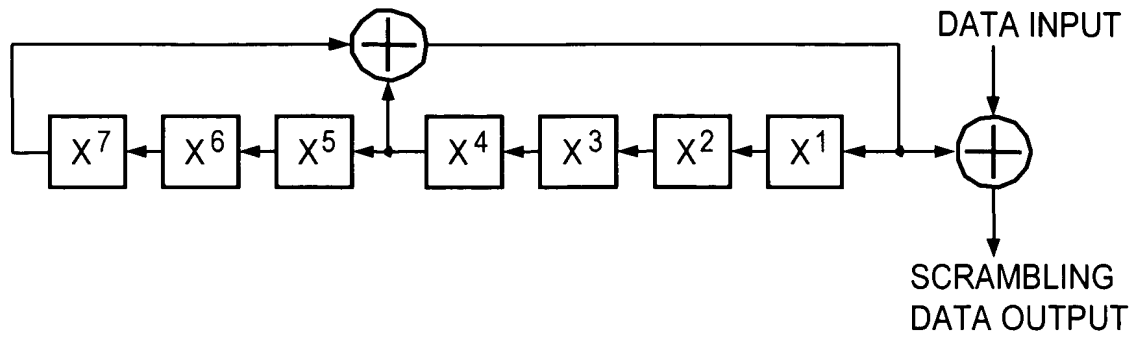


FIG. 34

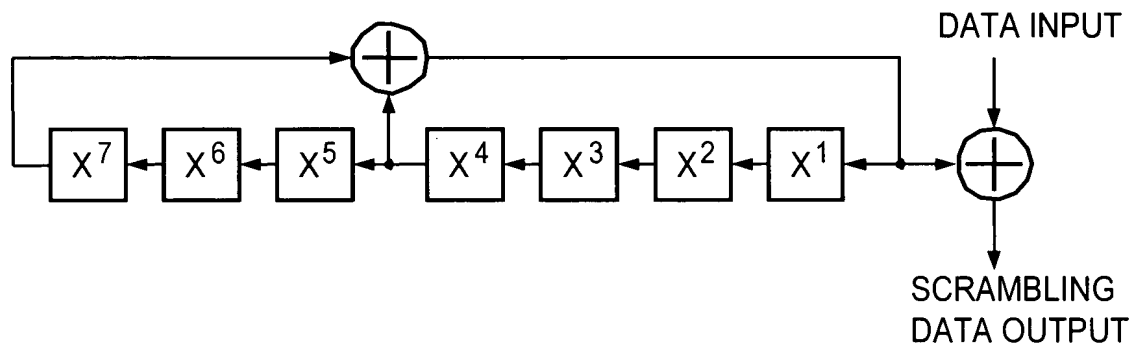
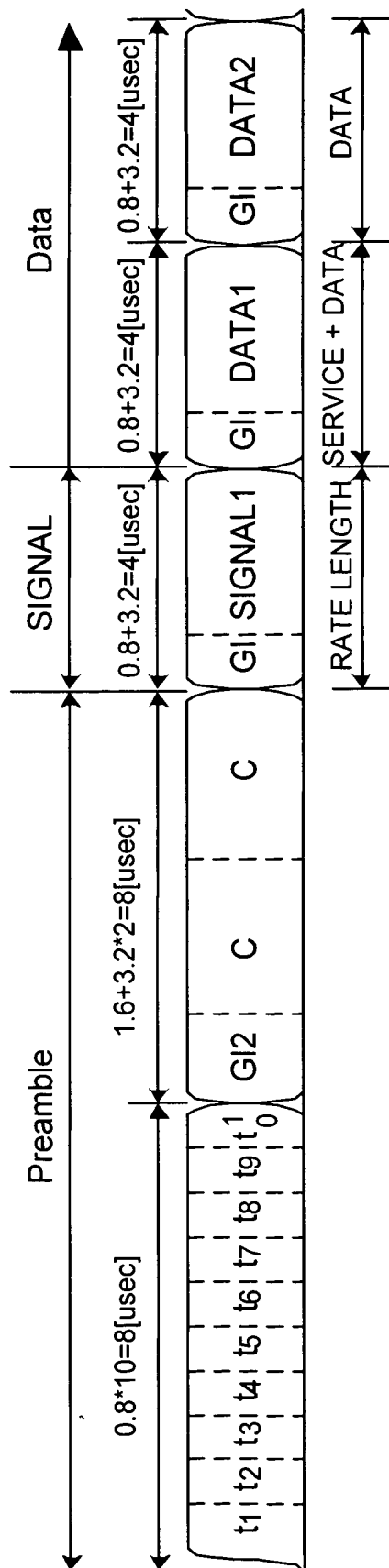


FIG. 35



31/32

FIG. 36

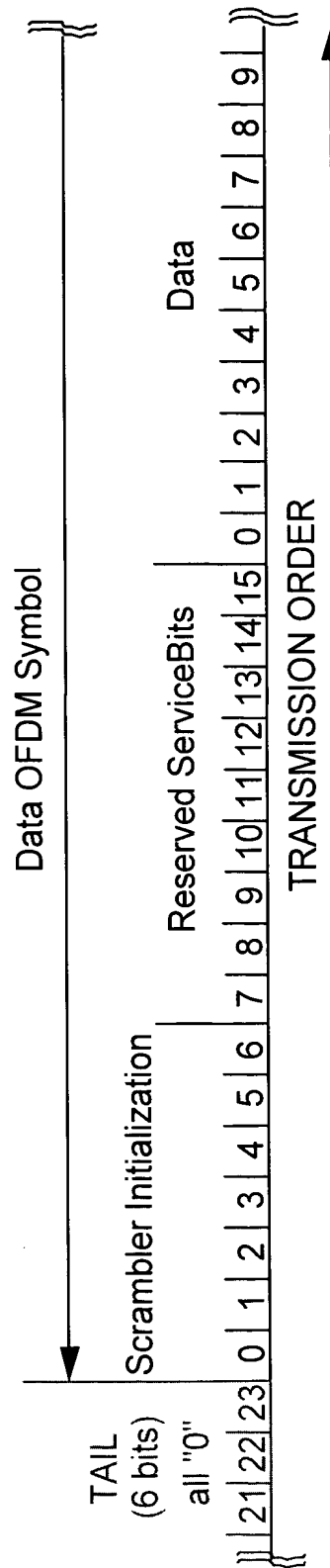
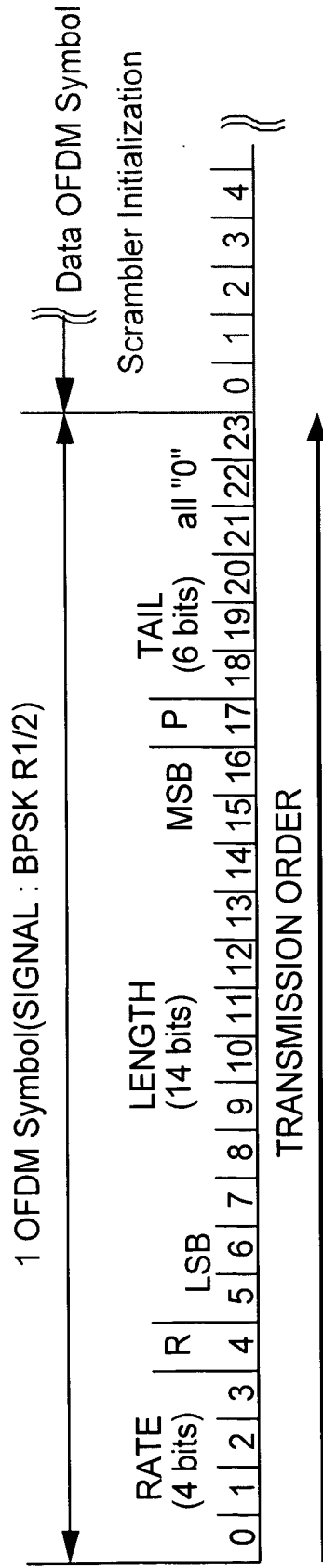


FIG. 37

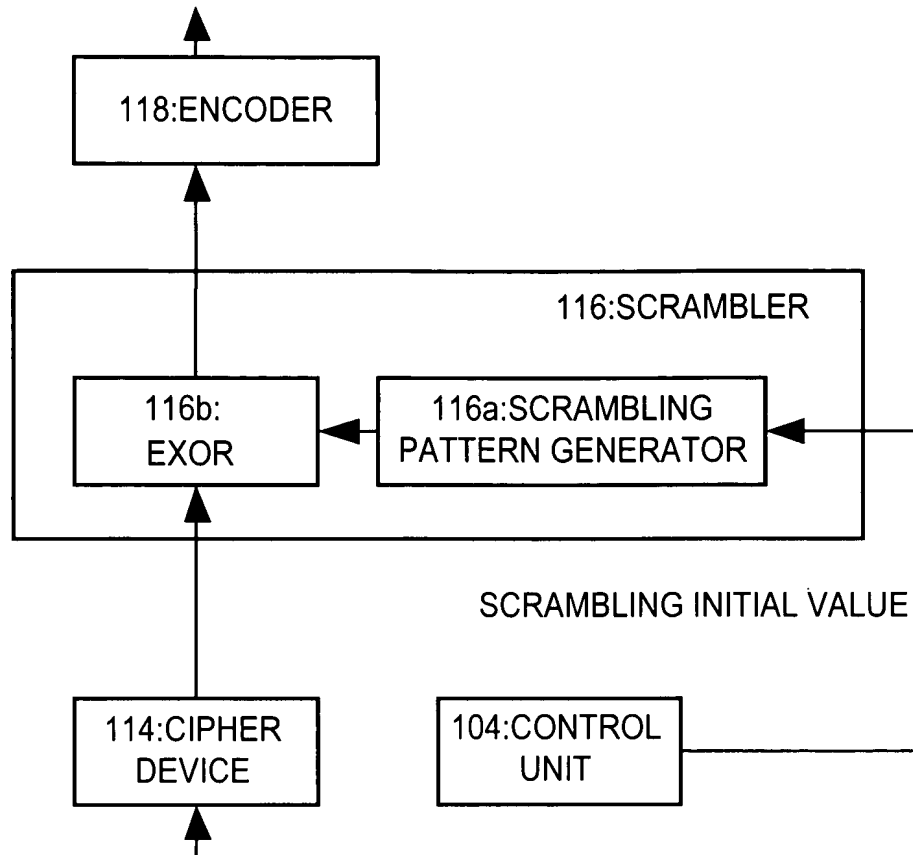


FIG. 38

